

PLS-11 FILTER & DELAY SUMMARY

PLS-11 FINAL STAGE DIAGRAM

0.001 CMOS DRIVER

0.002 5.1MHz CRYSTAL OSCILLATOR

0.003 5.1MHz TO 10MHz PLL

0.004 10MHz TO 5.1MHz

0.005 5.1MHz TO 10MHz

0.006 10MHz TO 5.1MHz

0.007 5.1MHz TO 10MHz

0.008 10MHz TO 5.1MHz

0.009 5.1MHz TO 10MHz

0.010 10MHz TO 5.1MHz

0.011 5.1MHz TO 10MHz

0.012 10MHz TO 5.1MHz

0.013 5.1MHz TO 10MHz